



Requisition #21-014

EMPLOYMENT POSTING

Job Title: Sr Electrical Engineer (FPGA)

Department/Work Area: **31 Engineering**

Overland Park, KS

Designs, develops, and supports electronic hardware at the board and programmable device (FPGA and CPLD) level that implements required functionality to meet product specifications, research and development or direct contract requirements.

Duties and Responsibilities:

Designs and documents circuits and/or firmware through preparation of specifications, circuit designs, design language (such as VHDL), lists of materials, test procedures, schematic capture, drawings and system operation of digital/analog electronic hardware, equipment and systems under low level supervision and an ability to collaborate in a team environment.

As part of normal job duties, will develop technical approach and perform detailed design. May mentor less experienced engineers or technicians.

Creates or contributes to project schedules and is responsible for tracking, maintaining, and meeting development schedules.

Supports Manufacturing through development and improvement of products, improved test methods, test procedures, consultation, and fault isolation.

Proven experience in the lab bringing up, testing and troubleshooting hardware circuits using test equipment including oscilloscopes, logic analyzers, power supplies and multimeters.

Participates in the Continuous Improvement Process.

Adhere to company policies and procedures. Cares for company assets.

Performs duties as assigned.

Education / Experience:

Bachelor of Science Degree in Electrical Engineering from ABET accredited school. Minimum 5 years of experience as an electronic detailed design engineer.

Skills: Sound knowledge of theory and mathematical basis of theory in area of specialization with the ability to apply theory and analytical techniques to achieve design objectives and specifications. Ability to communicate technical concepts to peers, subordinates, management and customers in both verbal and written form. Previous experience with FPGA design is required.

Knowledge of the Xilinx family of FPGA products is preferred.

Experience with design of digital systems/circuits for radio receivers, including digital demodulators, digital filters, AGCs; DSPs, FPGAs and/or ASIC elements is preferred.

Knowledge of microprocessor architectures, the C programming language, and the embedded software development process a plus.

Experience with government regulated design processes such as DO 254 is a plus.

Physical / Mental Demands

Ability to organize work efficiently and set correct priorities. Ability to lift, operate and instruct others in the use of tools and test equipment. Ability to independently construct and evaluate prototypes using hand and power tools.

Reasonable accommodations may be made to enable individuals with disabilities to perform the essential functions.

Any employee interested in this position may submit a Request for Position Consideration. This form can be obtained from Human Resources.

Alicia Worthey, Human Resources Director